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FORM PTO-1449  
(Modified)

U.S. Department of Commerce  
Patent and Trademark Office

Attorney Docket No.: MLNR-06501

Serial No.: 09/715,556

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**  
(Use Several Sheets If Necessary)

Applicant: Gwilym Luff

(37 CFR § 1.98(b))

Filing Date: November 17, 2000

Group Art Unit: 2631

**U.S. PATENT DOCUMENTS**

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
JP	AA	5,953,640	09/14/99	Meador et al.	455	73	04/30/97
	AB						
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**FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS**

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AQ							
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**OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)**

JP	AS	Hidehiko Kuroda et al., "Development of Low-Power Consumption RP/IF Single-Chip Transceiver IC for PHS," Doc. No. XP-000832839, pp. 161-167, Compound Semiconductor Device Division, NEC IC Microcomputer Systems, Ltd.
JP	AT	Angel Boveda et al., "GaAs Monolithic Single-Chip Transceiver," Doc. No. XP-000538492, pp. 31-34, IEEE, May 15, 1995.
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Examiner: *Jan Parilla*

Date Considered: 1/20/04

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## U.S. PATENT DOCUMENTS

Examiner Initials	Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
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Examiner:

*John A. Luff*

Date Considered:

1/29/04

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	AU							

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Examiner: *hann*

Date Considered: 1/20/04

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